



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re U.S. Patent Application of

YAMAMOTO et al.

Application Number: 10/817,004

Filed: April 5, 2004

For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE
AND MANUFACTURING METHOD THEREOF

Attorney Docket No. HITA.0536

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) Art Unit 2826
)
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) Examiner Ahmed N. Sefer
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)

Commissioner of Patents
P.O. Box 1450
Alexandria, VA 22313-1450

LETTER

Sir:

The below-identified communications are submitted in the above-captioned application or proceeding:

- ☒ Letter to Draftsperson
- ☒ 1 page of replacement drawings
- ☐ Check for _____.00

☒ The Commissioner is hereby authorized to charge payment of any fees associated with this communication, including fees under 37 C.F.R. 1.16 and 1.17 or credit any overpayment to **Deposit Account Number 08-1480**. A duplicate copy of this sheet is attached.

Respectfully submitted,

Stanley P. Fisher
Registration Number 24,344

Juan Carlos A. Marquez
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3110 Fairview Park Drive
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(703) 641-4200

June 27, 2007



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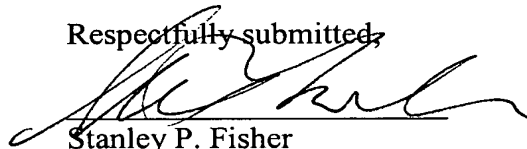
LETTER TO DRAFTSPERSON

Sir:

In response to the requirements set forth in the Notice of Allowance mailed on April 5, 2007, Applicants are submitting 1 sheet of replacement drawings.

Kindly direct any inquiries in connection with this matter to the undersigned at the below-listed address and telephone number.

Respectfully submitted,



Stanley P. Fisher
Registration Number 24,344

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